

Fig.1 (Prior Art)

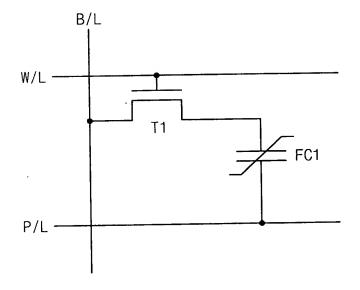


Fig.2 (Prior Art)

Device

Serial No. Unassigned
First Named Inventor: Hee Bok KANG

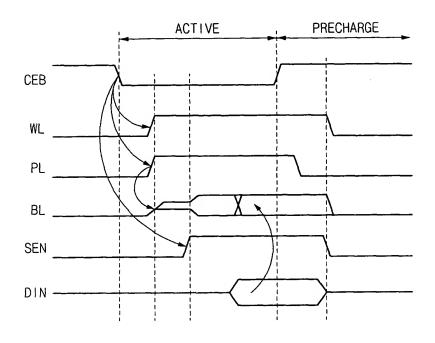


Fig.3A (Prior Art)

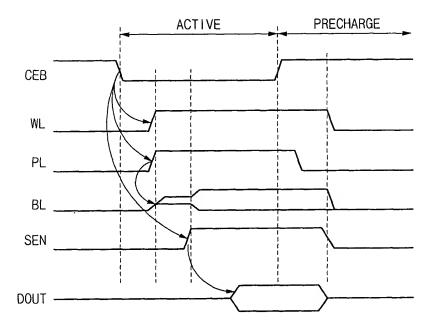


Fig.3B (Prior Art)

Device

Serial No. Unassigned

First Named Inventor: Hee Bok KANG

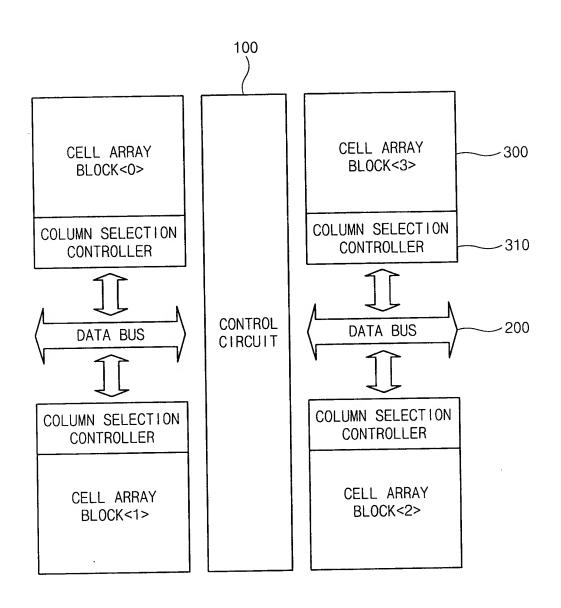


Fig.4

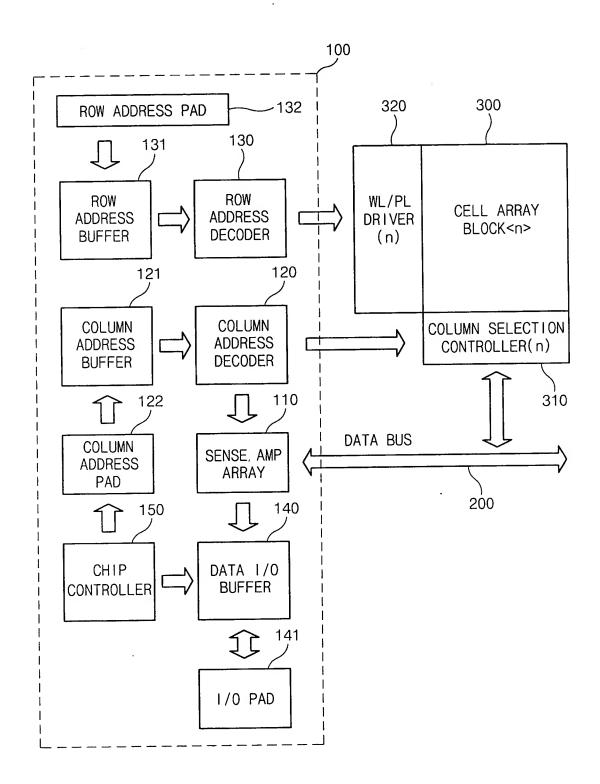


Fig.5

Device

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First Named Inventor: Hee Bok KANG

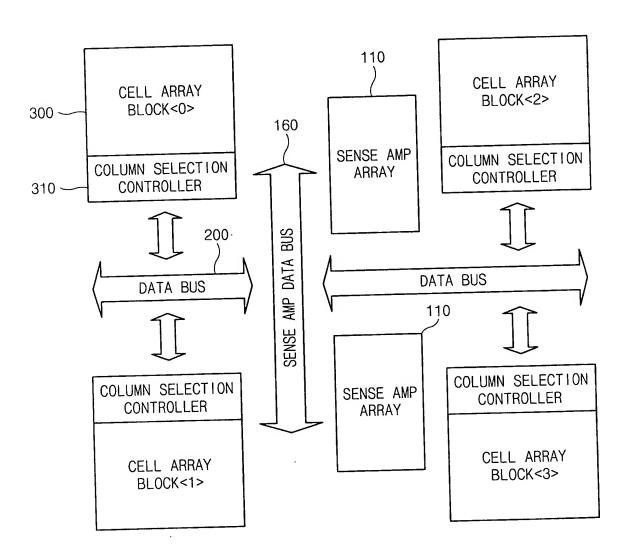


Fig.6

Device

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First Named Inventor: Hee Bok KANG

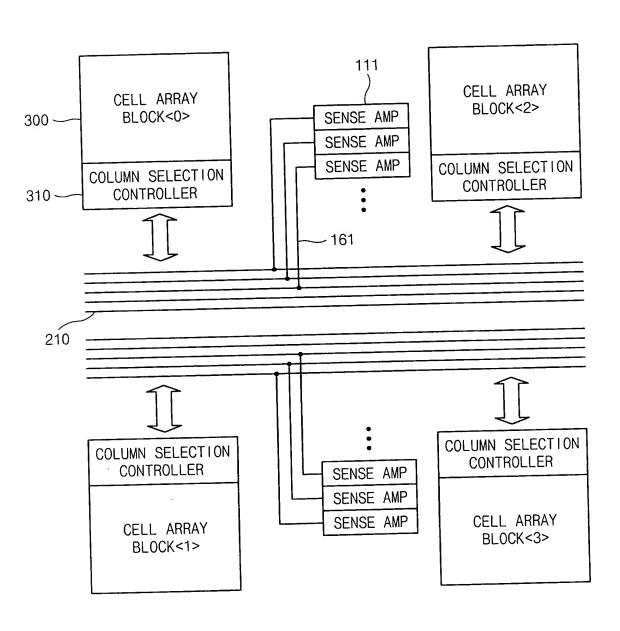


Fig.7

Device

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First Named Inventor: Hee Bok KANG

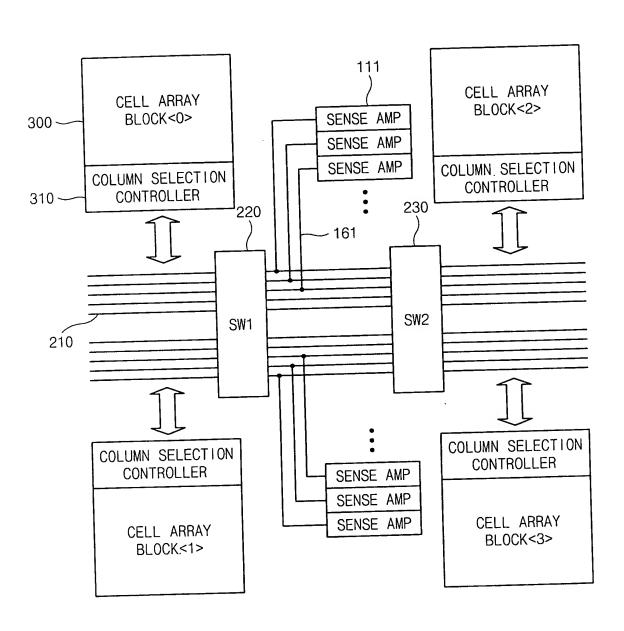


Fig.8A

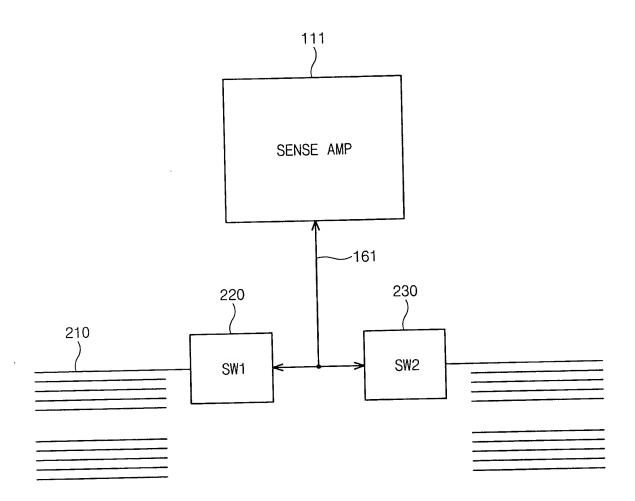


Fig.8B

Device

Serial No. Unassigned

First Named Inventor: Hee Bok KANG

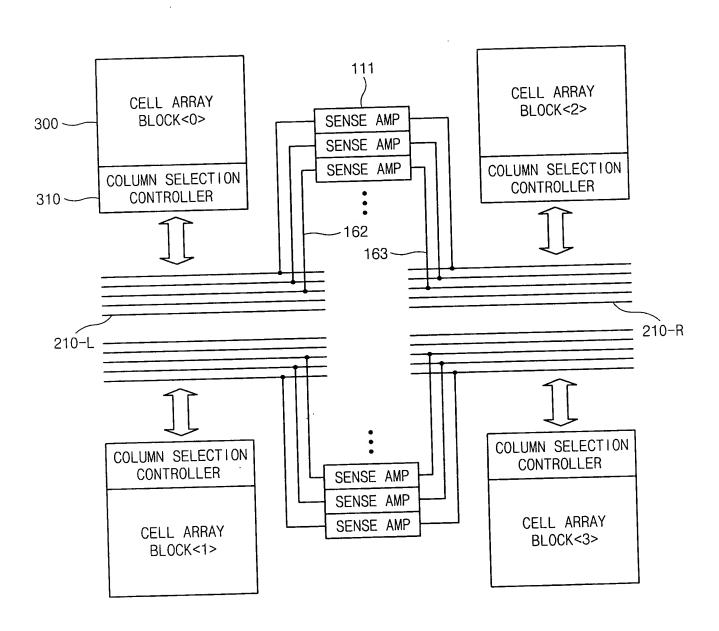


Fig.9A

Device

Serial No. Unassigned
First Named Inventor: Hee Bok KANG
Docket No. 40296-0011

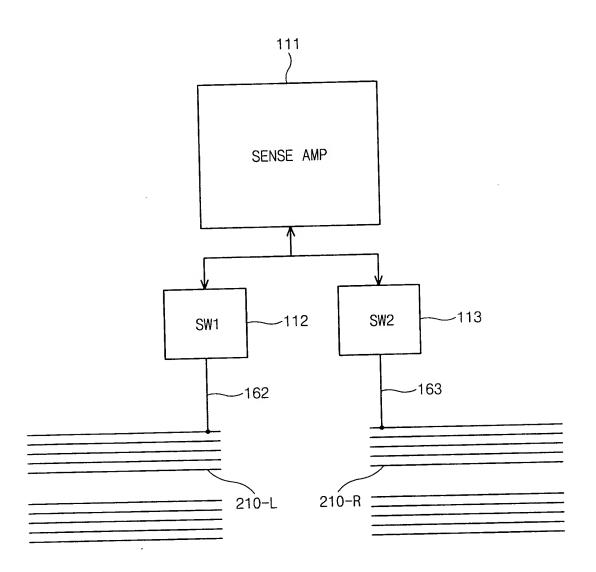
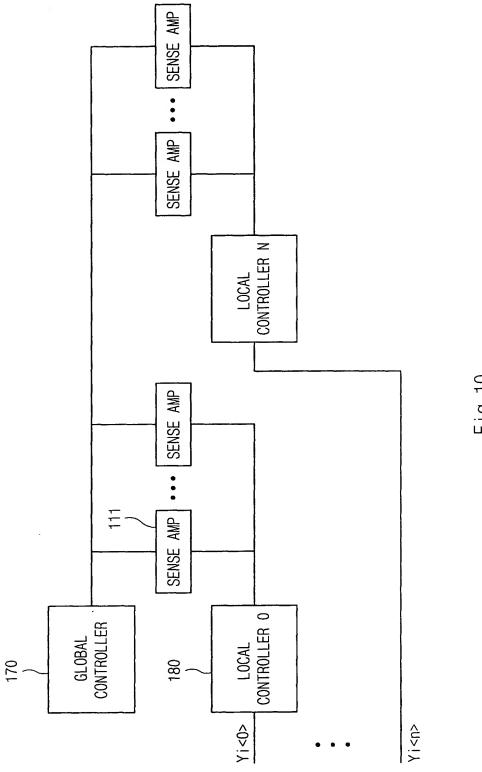


Fig.9B

Device

Serial No. Unassigned
First Named Inventor: Hee Bok KANG
Docket No. 40296-0011



Device

Serial No. Unassigned

First Named Inventor: Hee Bok KANG

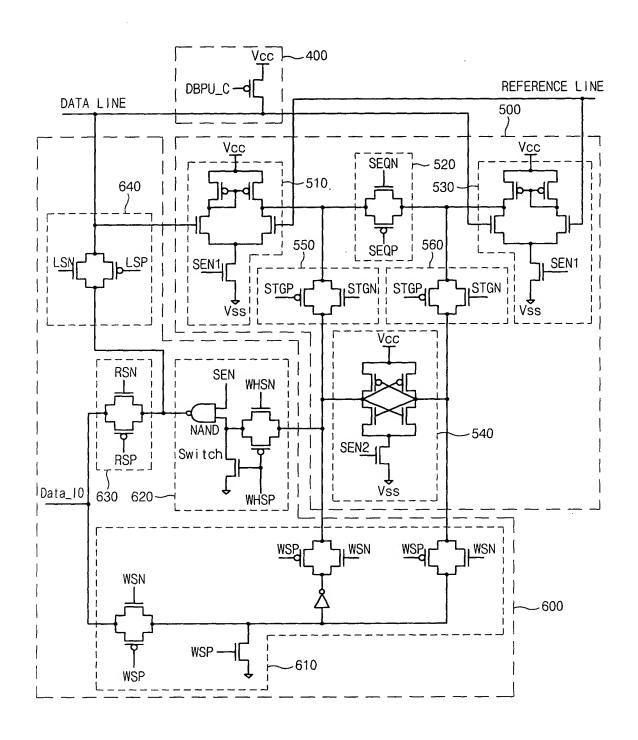


Fig.11A

Device

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First Named Inventor: Hee Bok KANG

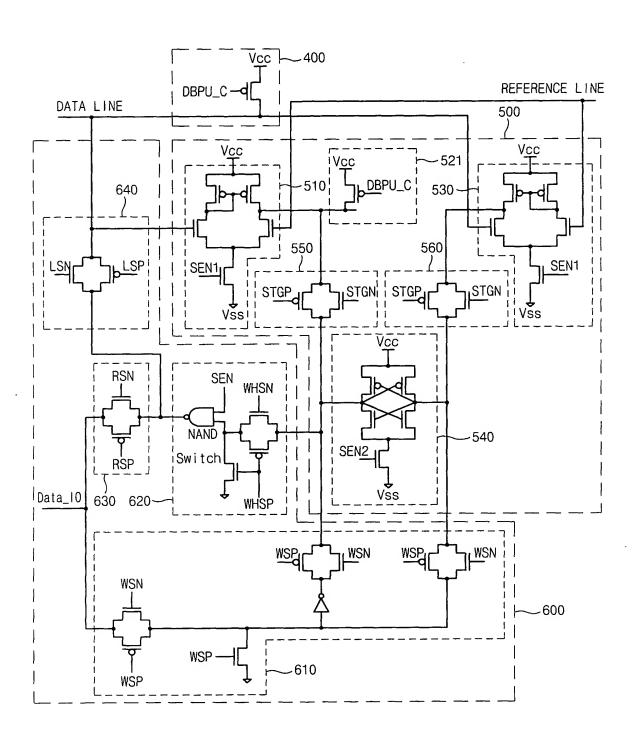


Fig.11B

Device

Serial No. Unassigned

First Named Inventor: Hee Bok KANG

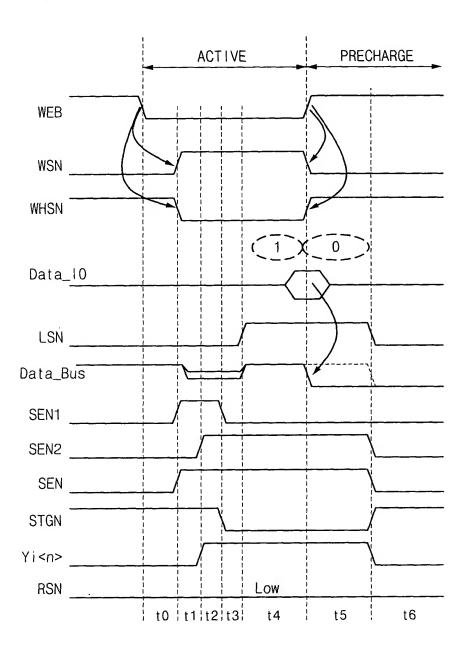


Fig.12

Device

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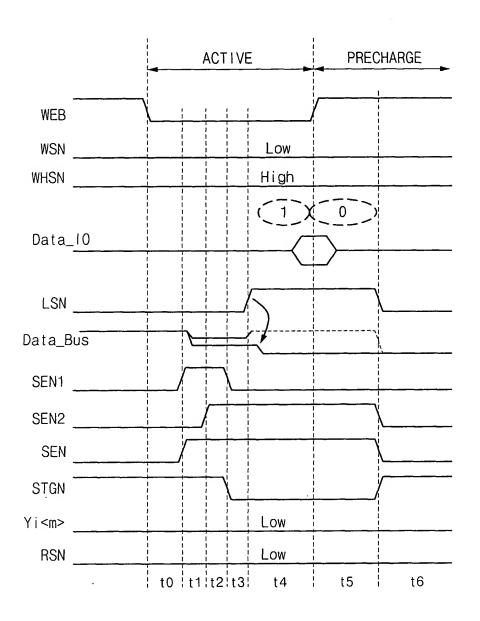


Fig.13

MAIN BIT LINE PULL-UP CONTROLLER — 330

MAIN BIT LINE LOAD CONTROLLER (0) — 340

SUB CELL BLOCK (0) — 350

MAIN BIT LINE LOAD CONTROLLER (K)

SUB CELL BLOCK (N)

COLUMN SELECTION CONTROLLER — 310

Fig.14

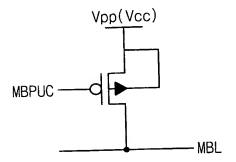


Fig.15

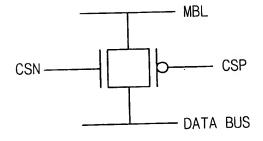


Fig.16

Device

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Docket No. 40296-0011

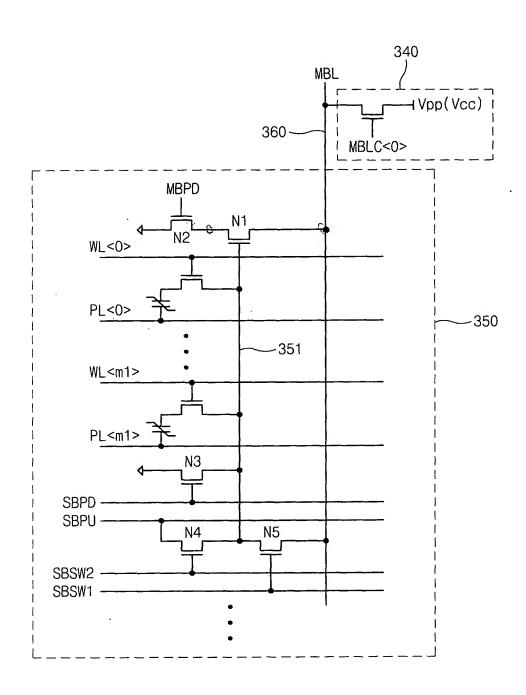


Fig.17

Device

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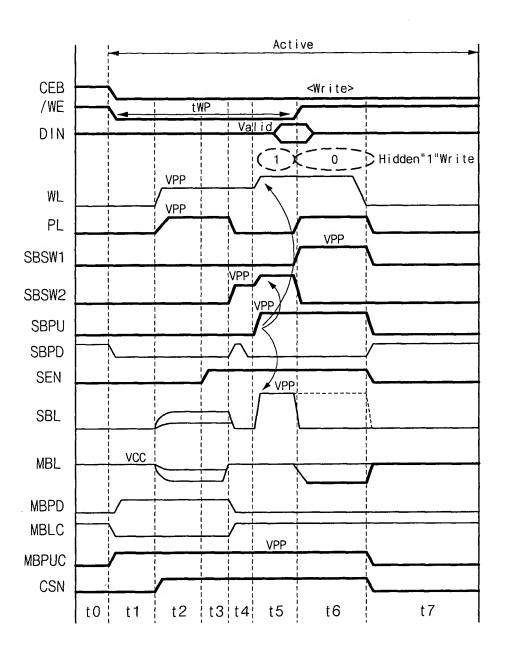


Fig.18A

Device

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First Named Inventor: Hee Bok KANG

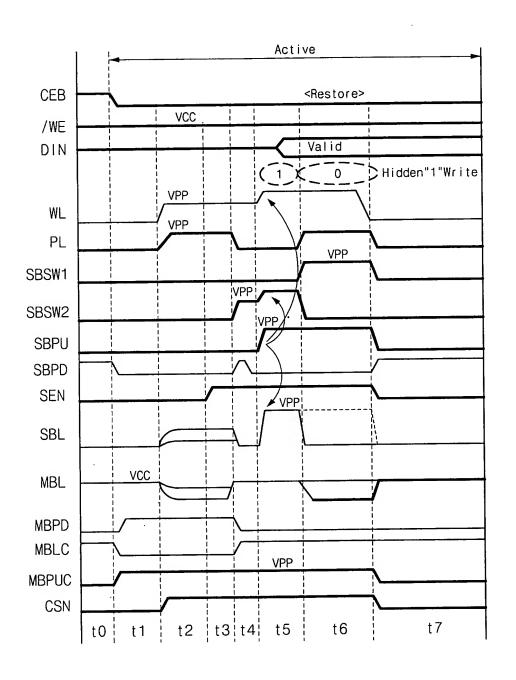


Fig.18B

Serial No. Unassigned
First Named Inventor: Hee Bok KANG

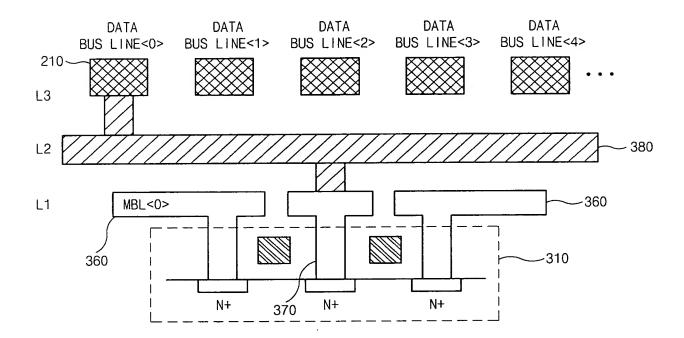


Fig.19

Device

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First Named Inventor: Hee Bok KANG

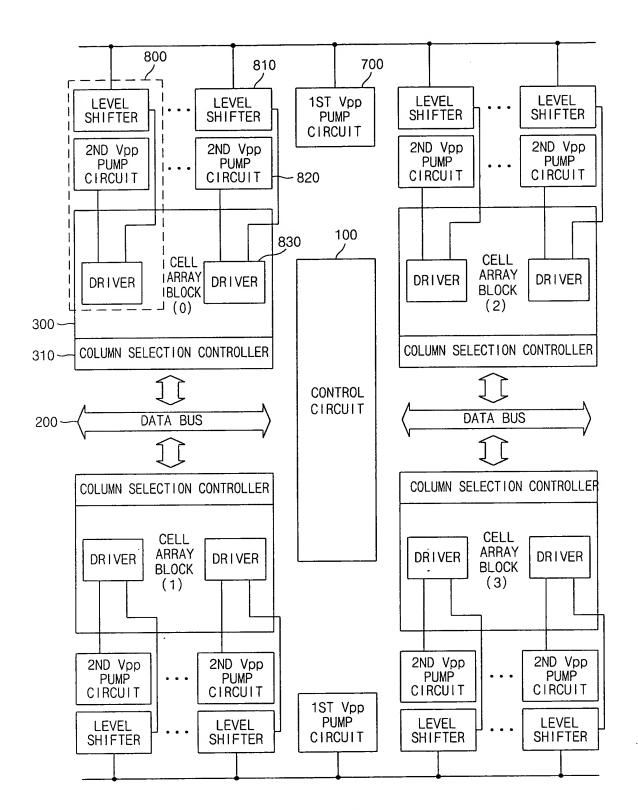


Fig.20

Device

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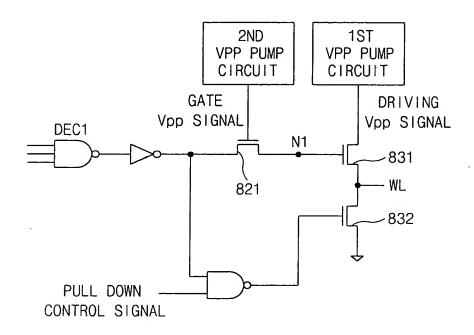


Fig.21

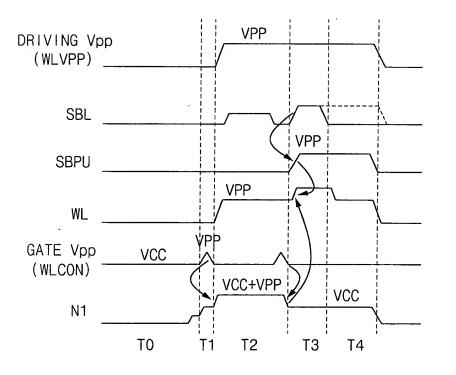


Fig.22